

## PLC Test Station Status Report

**Date: May 15, 2019**

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1. Hall A PLC for right HRS arm configured.
  - 1.1. Controller (model 1769-L30ER) firmware version 28 installed.
  - 1.2. Hall A development subnet IP address assigned to PLC
    - 1.2.1. hostname: *ha-right-arm-plc*
    - 1.2.2. IP address: 129.57.196.14
2. PLC software installed on DSG's Hall A development PC (DSG-HALLA-1).
  - 2.1. PC operating system is Windows 10
  - 2.2. Studio 5000 version 28 and all supporting software (RSLinx, activation manager) installed.
3. Hall A PLC wired to test TTL digital input and output modules.
  - 3.1. Modules wired to external benchtop 5 V supply.
  - 3.2. Channels on TTL output module wired to corresponding channel on TTL input module to allow simultaneous testing of multiple channels.
4. Routine to test TTL input/output under development.